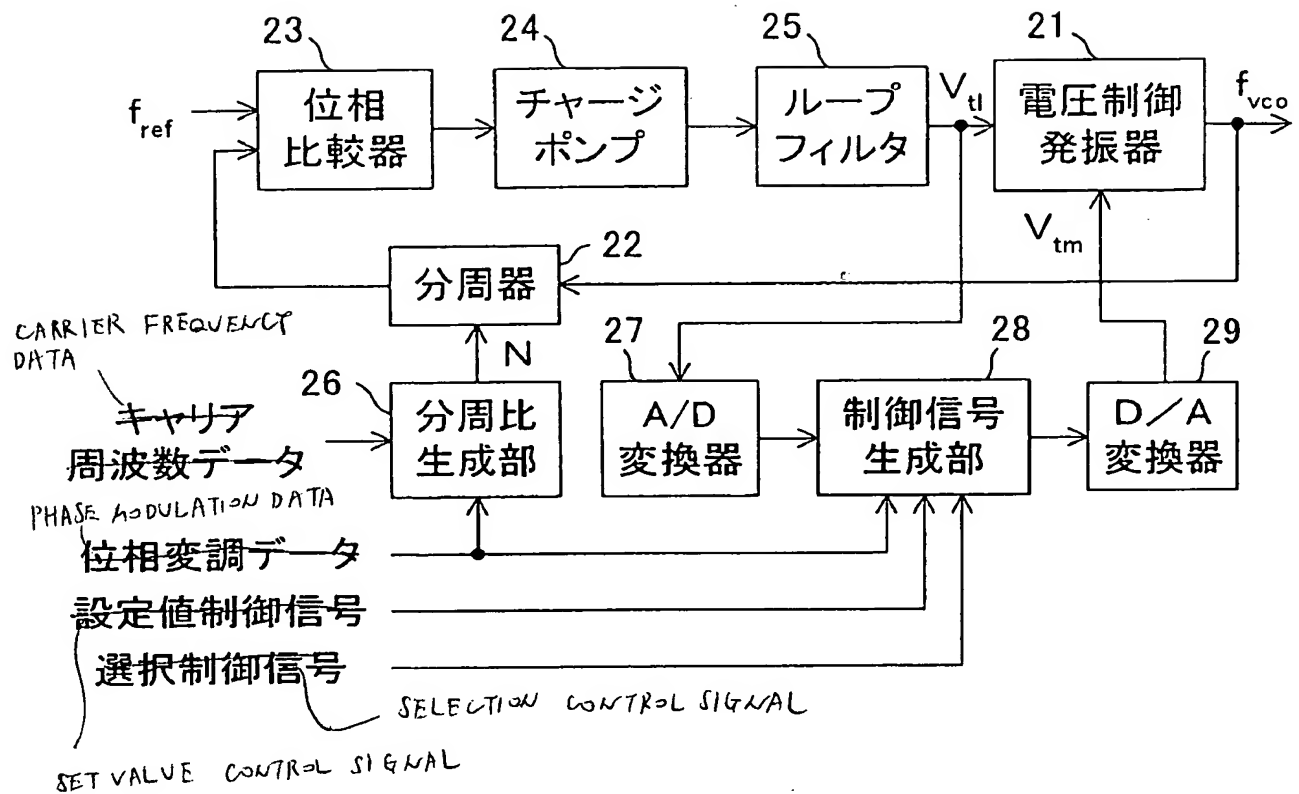
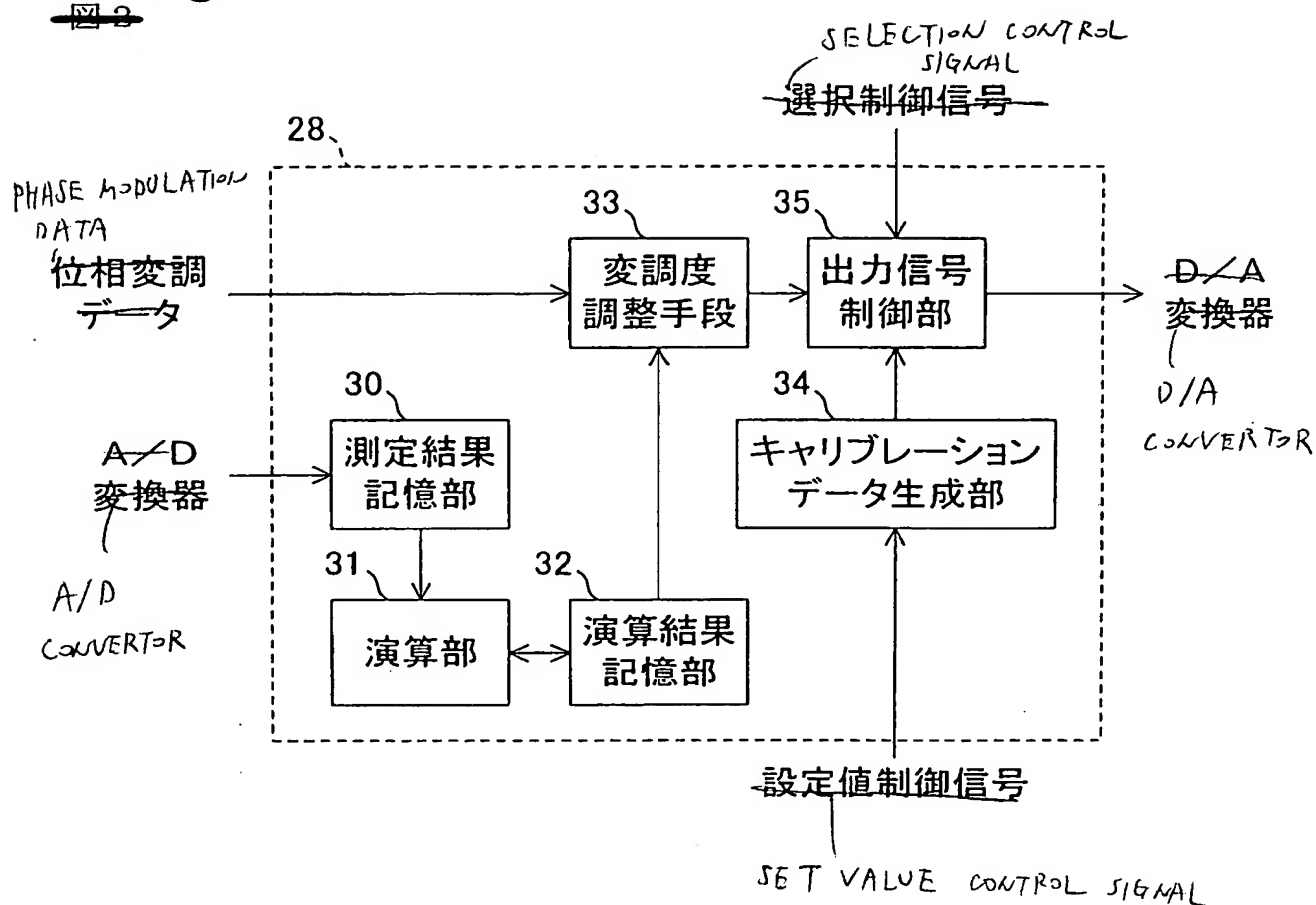


Fig. 1



- 21 VOLTAGE CONTROLLED OSCILLATOR
- 22 FREQUENCY DIVIDER
- 23 PHASE COMPARATOR
- 24 CHARGE PUMP
- 25 LOOP FILTER
- 26 FREQUENCY DIVIDING RATIO GENERATION PART
- 27 A/D CONVERTER
- 28 CONTROL SIGNAL GENERATION PART
- 29 D/A CONVERTER

Fig. 2



- 30 MEASUREMENT RESULT STORAGE PART
- 31 OPERATION PART
- 32 OPERATION RESULT STORAGE PART
- 33 MODULATION FACTOR ADJUSTMENT UNIT
- 34 CALIBRATION DATA GENERATION PART
- 35 OUTPUT SIGNAL CONTROL PART

Fig. 3

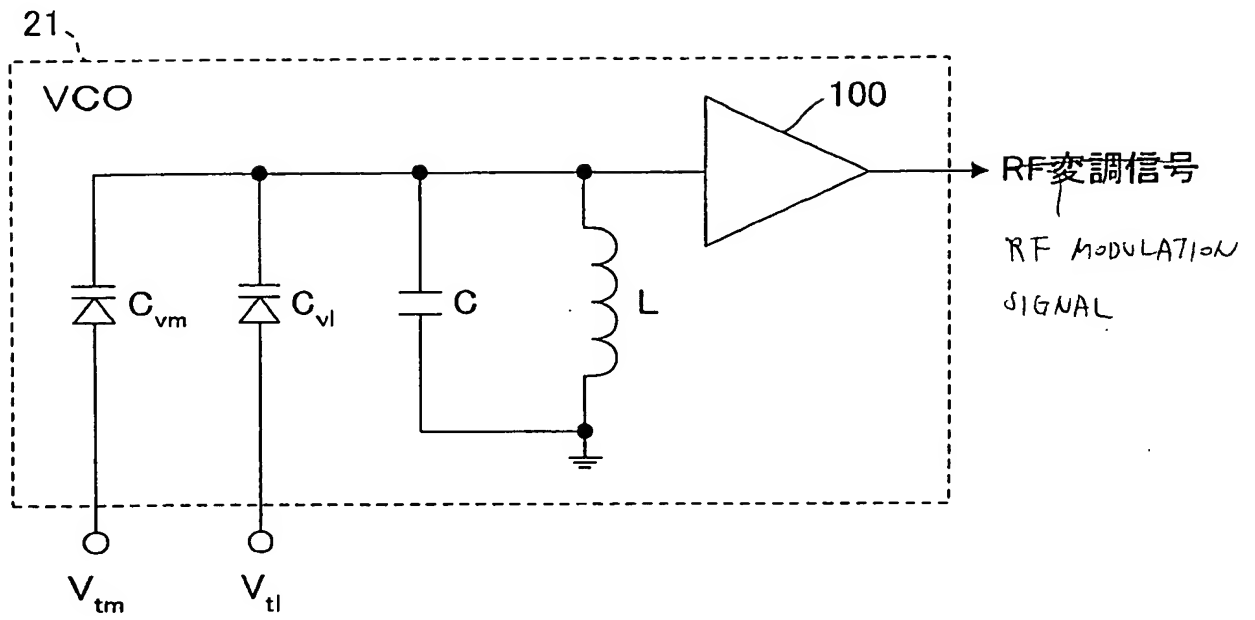


Fig. 4

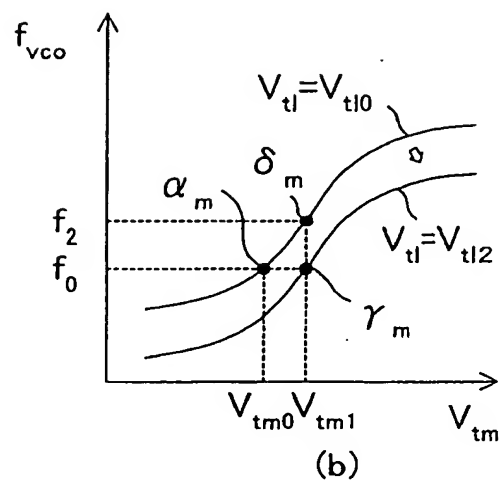
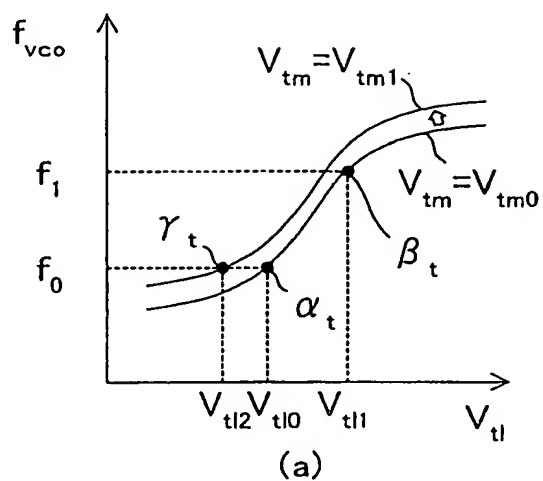


Fig. 5

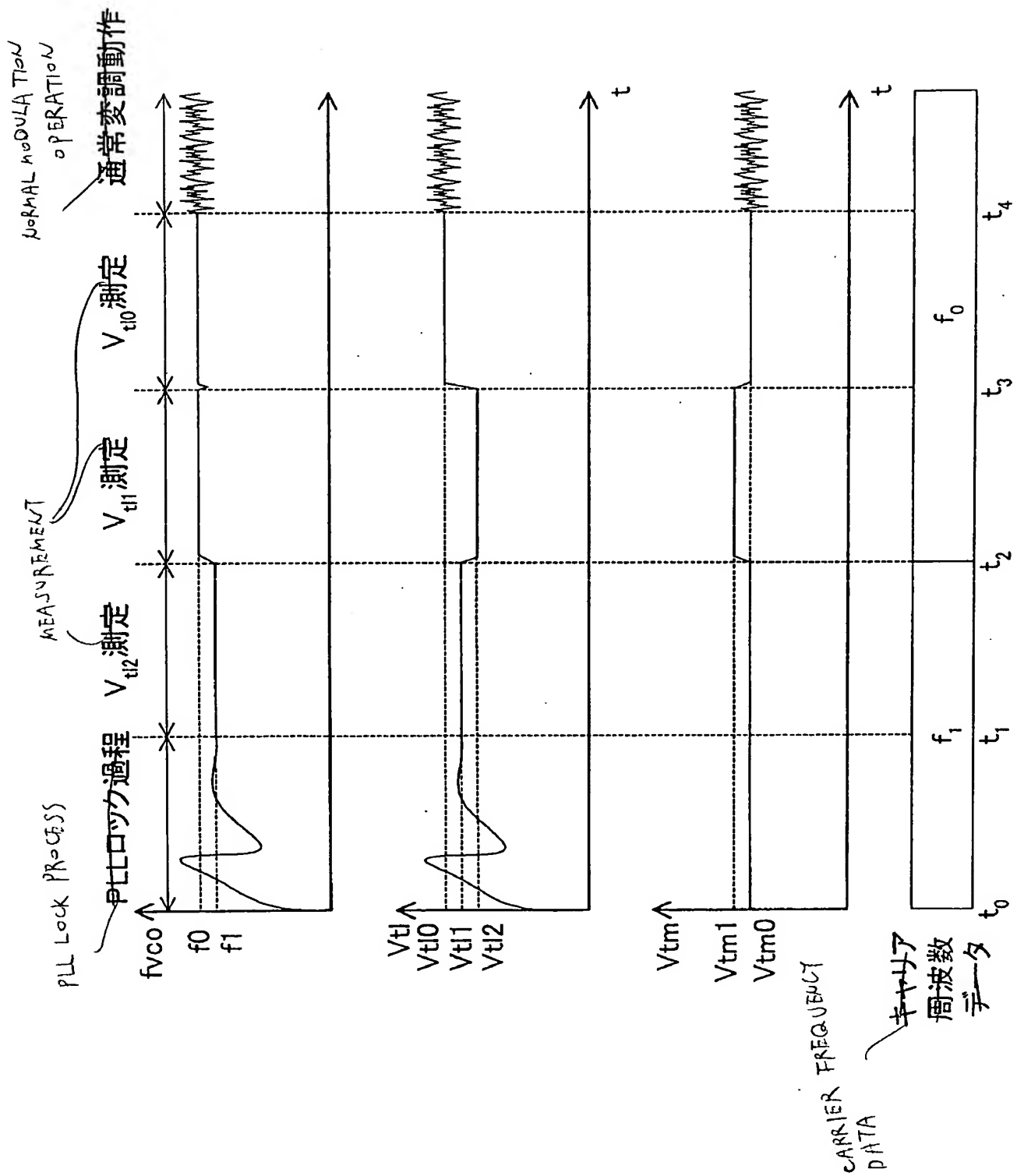
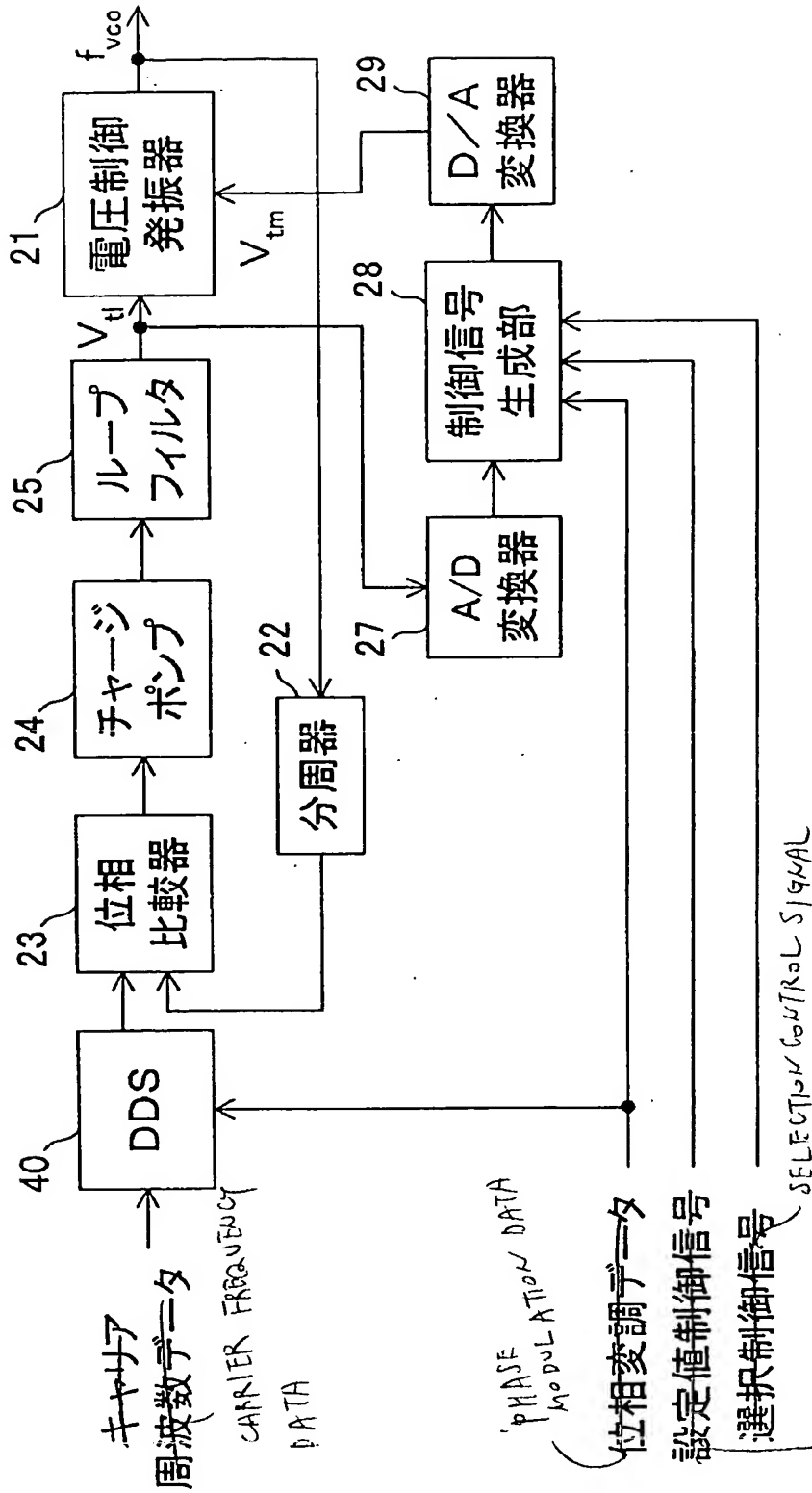


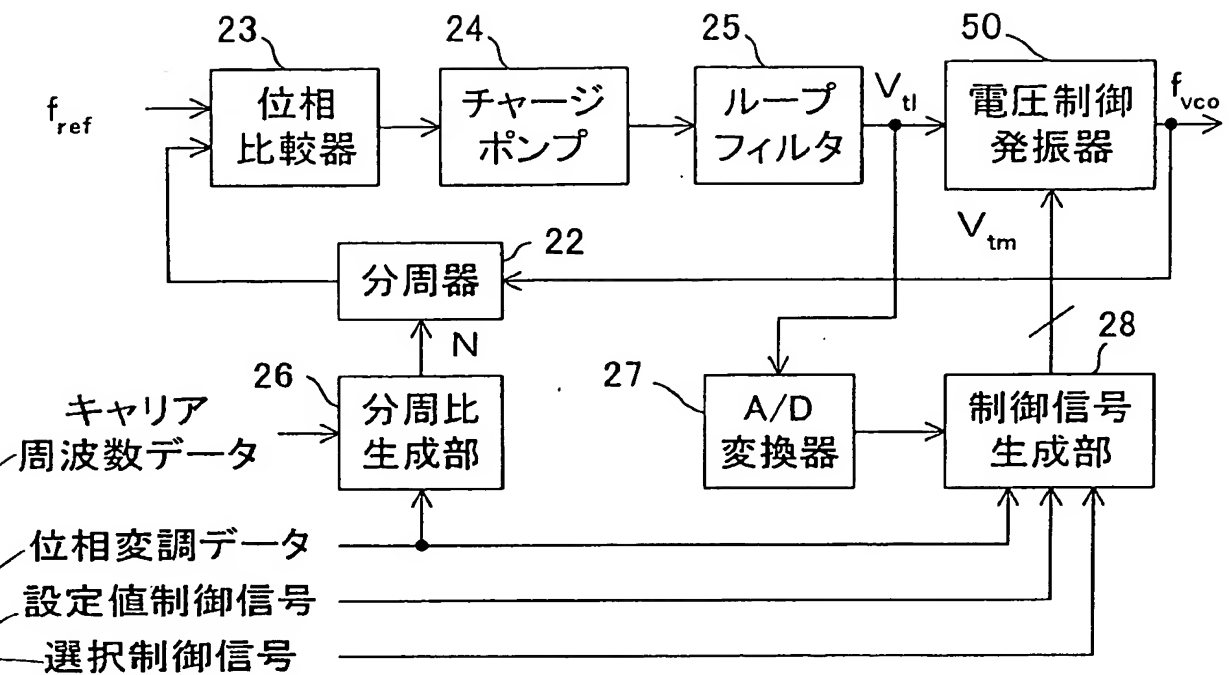
Fig. 6



- | | | | | | | | | |
|------------|---------|---------|--------|--------------------------|----|-------------------------------|----|--------------------------------|
| キャリア周波数データ | 位相変調データ | 設定値制御信号 | 選択制御信号 | SET VALVE CONTROL SIGNAL | 21 | VOLTAGE CONTROLLED OSCILLATOR | 25 | LOOP FILTER |
| | | | | | 22 | FREQUENCY DIVIDER | 27 | A/D CONVERTER |
| | | | | | 23 | PHASE COMPARATOR | 28 | CONTROL SIGNAL GENERATION PART |
| | | | | | 24 | CHARGE PUMP | 29 | D/A CONVERTER |

Fig. 7

図7



- | | | |
|--------------------------|----|--|
| CARRIER FREQUENCY DATA | 22 | FREQUENCY DIVIDER |
| PHASE MODULATION DATA | 23 | PHASE COMPARATOR |
| SET VALUE CONTROL SIGNAL | 24 | CHARGE PUMP |
| SELECTION CONTROL SIGNAL | 25 | LOOP FILTER |
| | 26 | FREQUENCY DIVIDING RATIO GENERATION PART |
| | 27 | A/D CONVERTER |
| | 28 | CONTROL SIGNAL GENERATION PART |
| | 50 | VOLTAGE CONTROLLED OSCILLATOR |

Fig. 8

図8

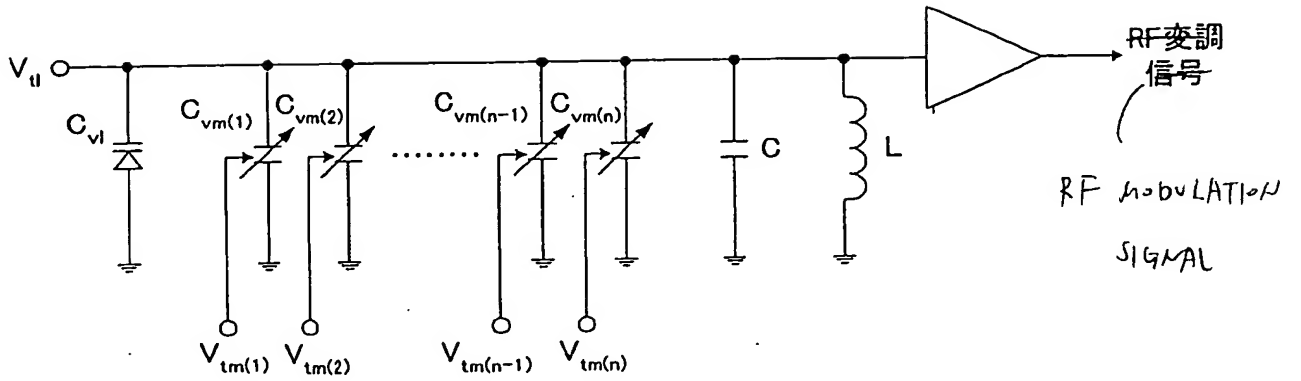


Fig. 9

図9

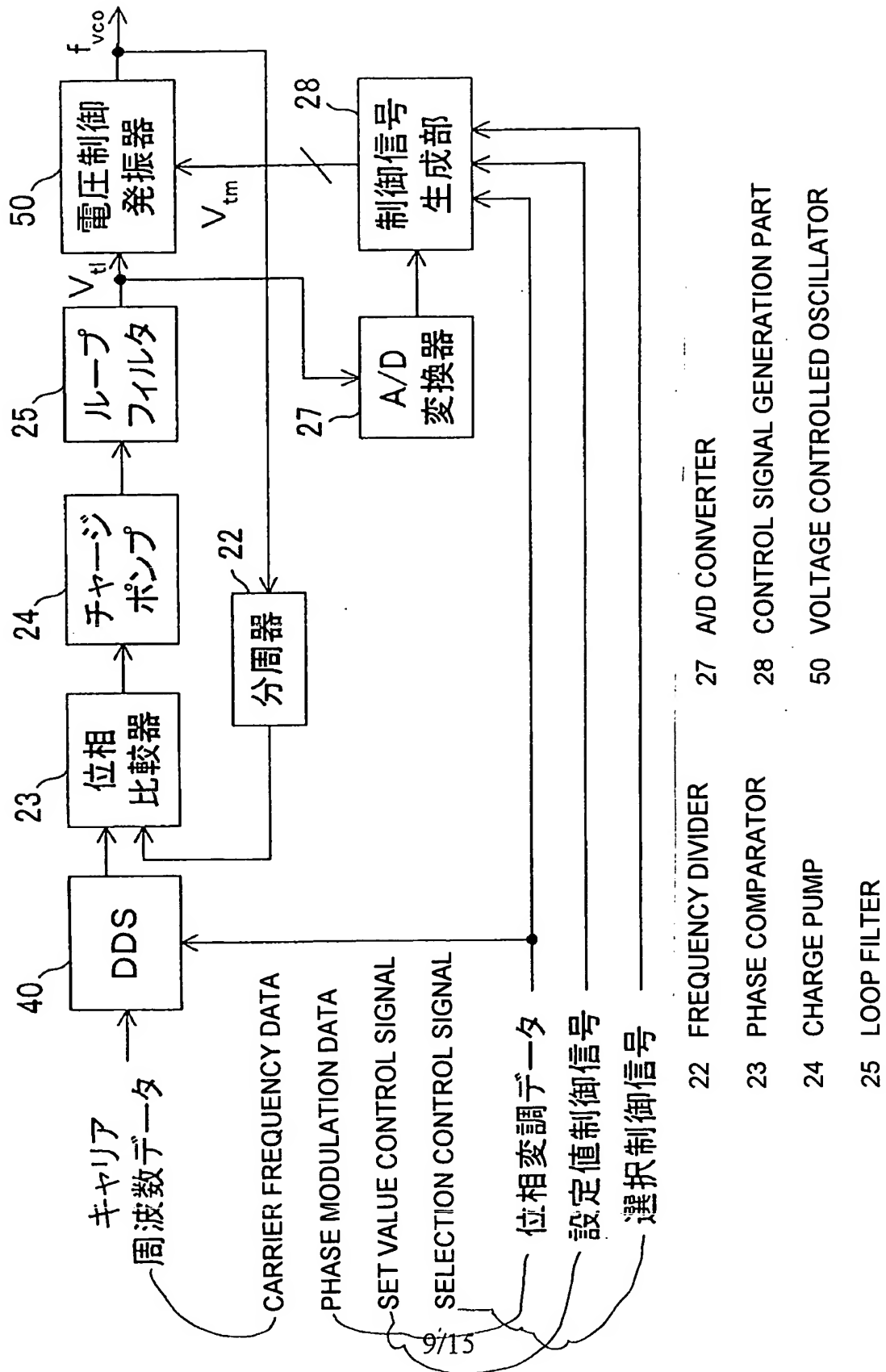
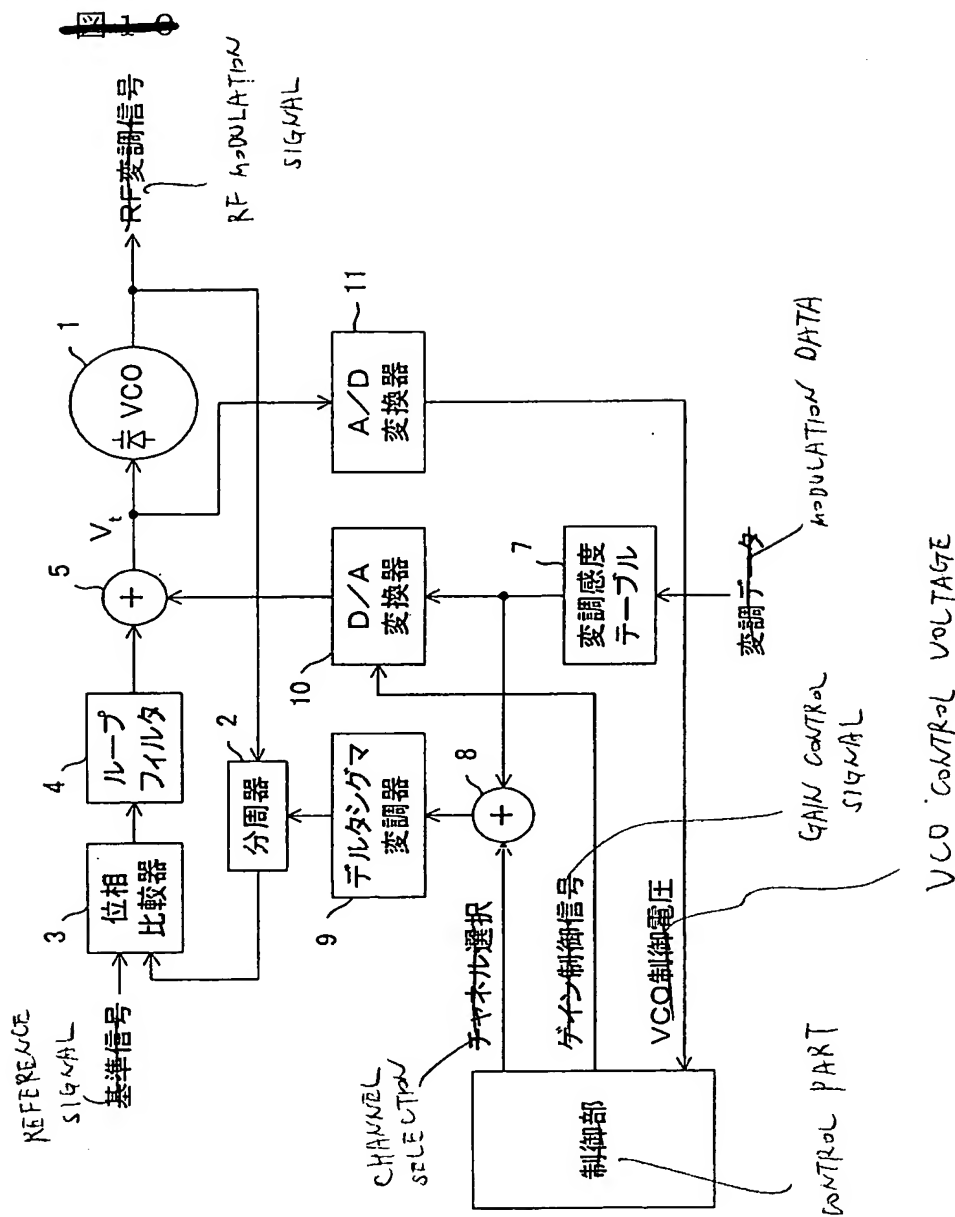


Fig. 10



2 FREQUENCY DIVIDER

3 PHASE COMPARATOR

4 LOOP FILTER

7 MODULATION SENSITIVITY TABLE

9 DELTA AND SIGMA MODULATOR

10 D/A CONVERTER

11 A/D CONVERTER

Fig. 11

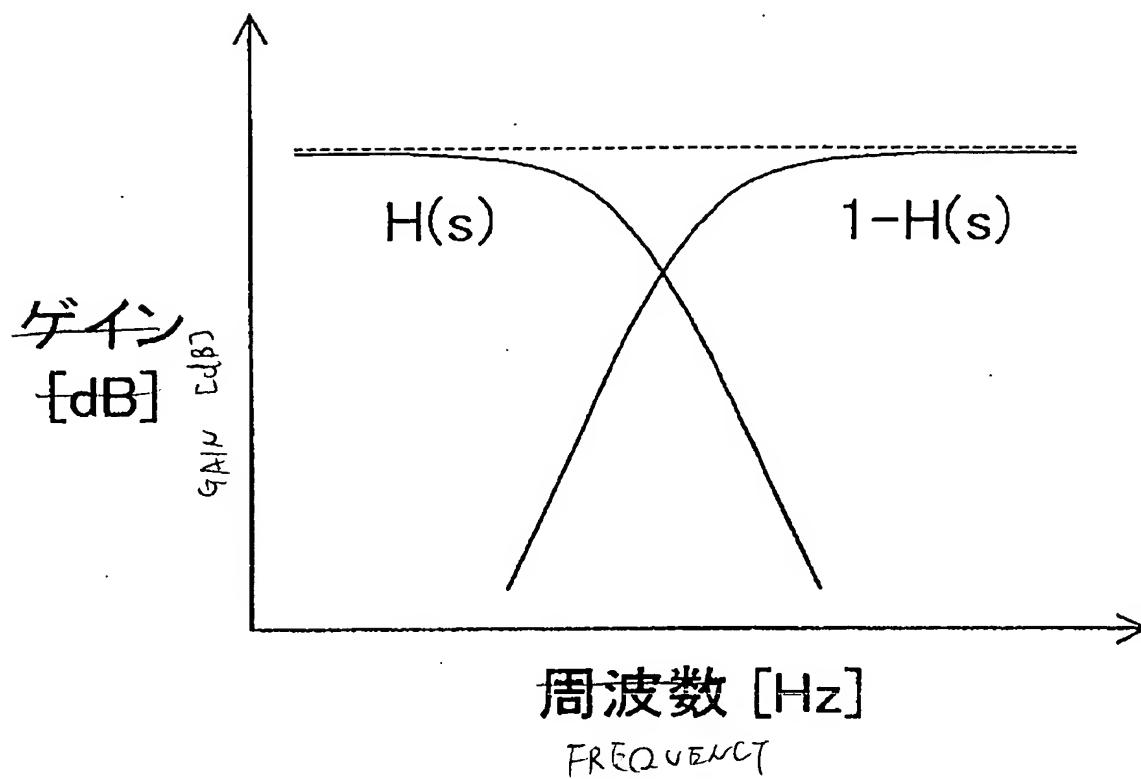
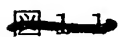


Fig. 12

12

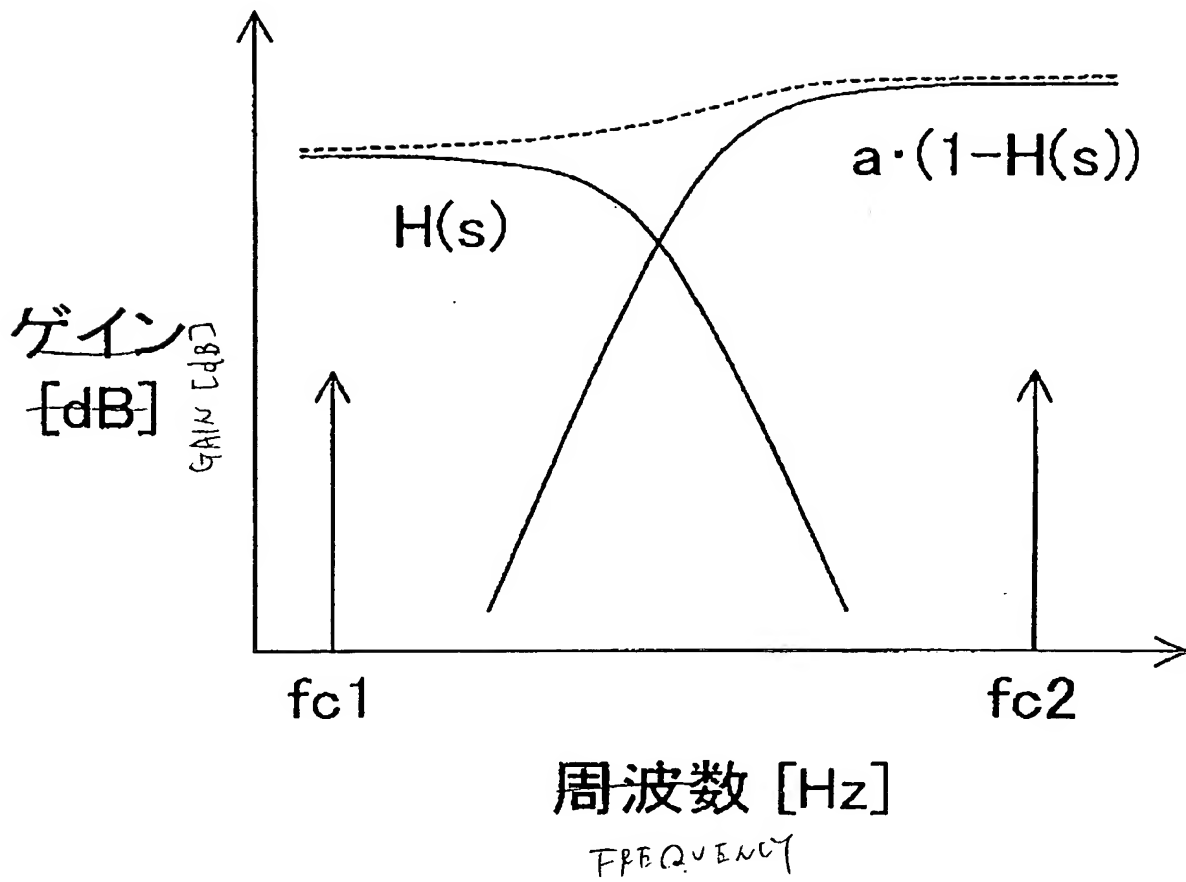


Fig. 13

图 13

周波数

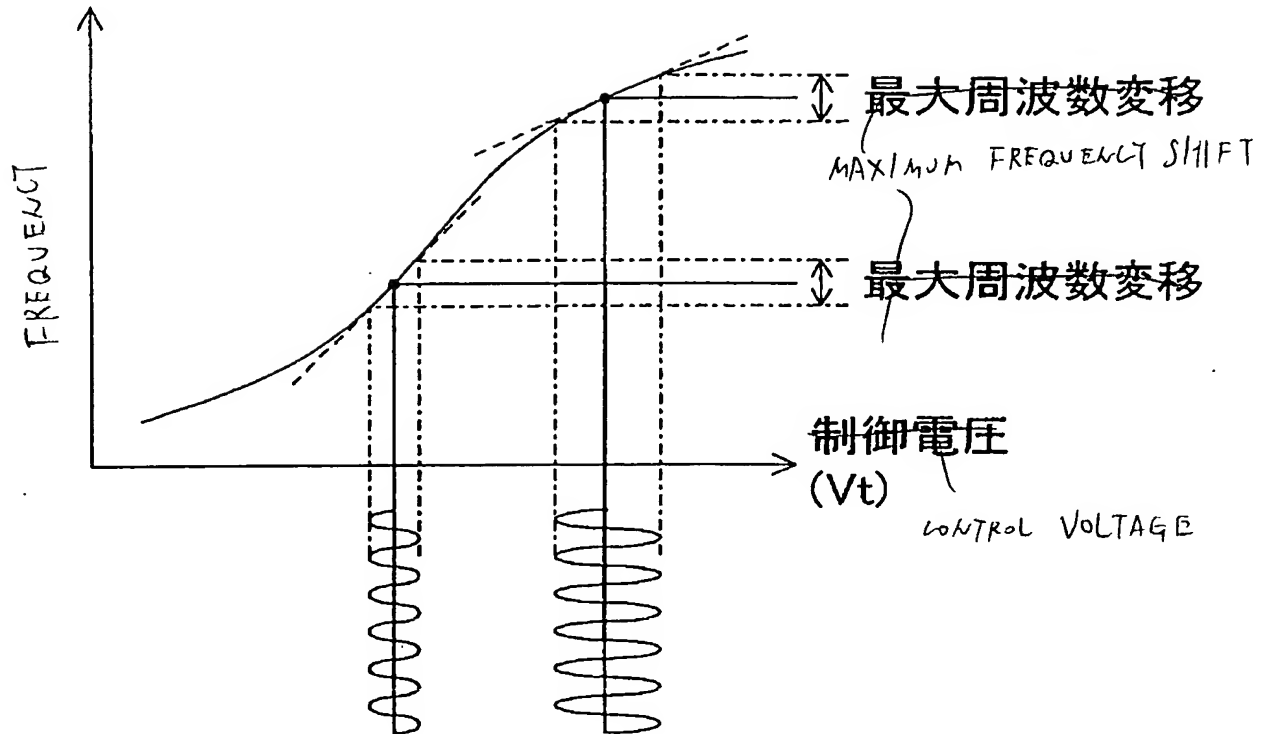


Fig. 14

~~図 14~~

変調感度

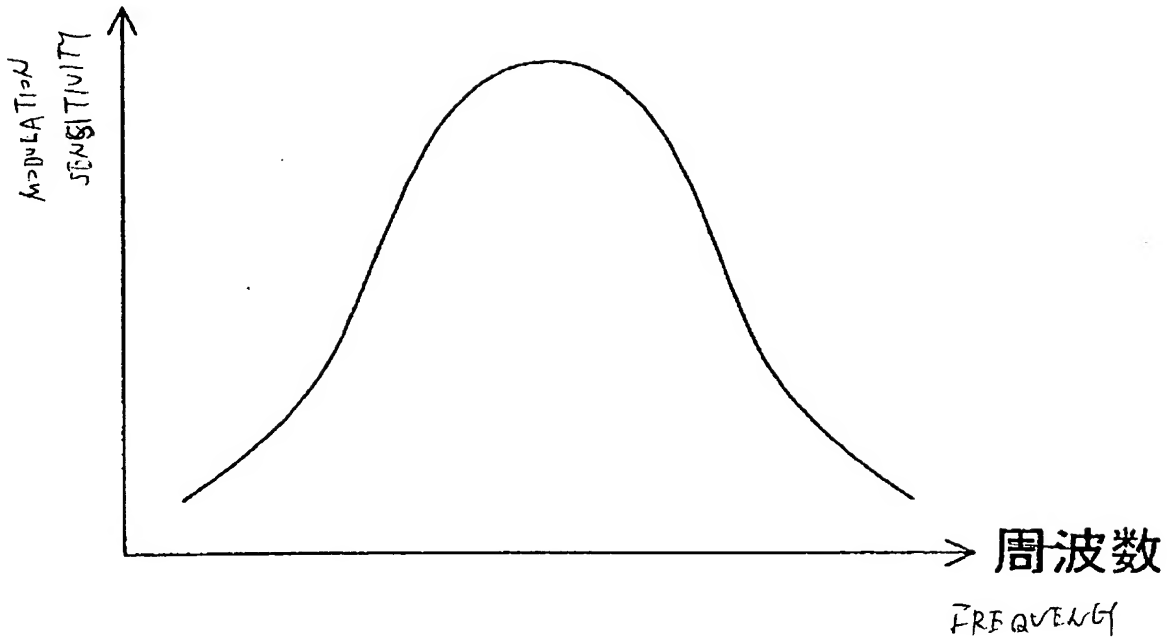


Fig. 15

FIG. 15

